

NONPROVISIONAL PATENT
APPLICATION TRANSMITTAL RULE §1.53(b)
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Customer No. 004372
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Docket No. 024016-00070
Date: September 25, 2003

Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is a nonprovisional patent application:

For (Title): SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND SELF-TEST
METHOD OF MEMORY MACRO

By (Inventors): Katsuhiko ITAKURA (Kasugai, Japan)

- ☒ 39 pages of Specification/Claims 1-21/Abstract are attached.
☒ Formal drawings (Fig(s). 1-12: 11 sheet(s)) is/are attached.
☒ A Declaration and Power of Attorney is attached.
☒ An assignment of the invention to Fujitsu Limited is attached, along with Form PTO-1595 and a check for \$40.00.
☒ An Information Disclosure Statement is attached, along with Form PTO-1449, and 1 reference(s).
☒ Priority of foreign application No. 2002-284817 filed September 30, 2002 in Japan is claimed under 35 U.S.C. §119.
☒ A certified copy of the above corresponding foreign application is attached.

The filing fee is calculated below and includes claim status after entry of any Preliminary Amendment noted above:

FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	21 - 20	= 1
INDEP CLAIMS	8 - 3	= 5
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS		

SMALL ENTITY

RATE	FEE
	\$ 375
x 9 =	\$
x 42 =	\$
+140 =	\$
TOTAL	\$

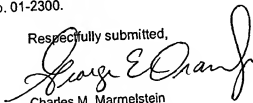
OR
OR
OR
OR
OR

LARGE ENTITY

RATE	FEE
	\$ 750
x 18	\$ 18
x 84	\$ 420
+280	\$
TOTAL	\$ 1,188

- ☒ Check# 366186 in the amount of \$1,228.00 (\$1,188.00 for the filing fee and \$40.00 for the Assignment Recordation Fee) is attached. Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300.

Respectfully submitted,


Charles M. Marmelstein
Registration No. 25,895

CMM/rkc

27931

09/25/03
17667 U.S. PTO

21906 U.S. PTO

10/669421

09/25/03